

PLUSETECH DDR4 16GB 2666 SO-DIMM Datasheet**PSD4B16HA26SN-16G**

Module Part Number	PSD4B16HA26SN-16G
Memory Type	DDR4
Module Type	Unbuffered SO-DIMM
Module Density	16GB
Data Width	x64
Data Rate	2666 MT/s
V_{DD} Voltage	1.2V
Interface	260-pin
Number of Ranks	2
SDRAM Device Width	X8
CAS Latency	CL19
Operation Temperature	0°C ~ +85°C
RoHS	Yes
Standard	JEDEC

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Features

- JEDEC Standard
- 260-pin/ small outline dual in-line memory module(SO-DIMM)
- Fast data transfer rates: PC4-2666/ backward compatible PC4-2400/ PC4-2133
- 16GB (1024M x 8 x 16PCS)
- $V_{DD} = 1.2V$ (1.14-1.26V)
- Backward compatible to $V_{DD} = 1.2V \pm 0.06V$
- $V_{DDSPD} = 2.5V$
- Nominal and dynamic on-die termination(ODT) for data/ strobe/ and mask signals
- Dual-rank
- 16 internal banks
- Bi-Directional Differential Data Strobe
- Programmable CAS latency 15/ 16/ 17/ 18/ 19 supported

- On board I²C with integrated serial presence-detect(SPD) EEPROM
- JEDEC standard 78ball FBGA(x8)
- Gold edge contacts
- This product in compliance with the RoHS directive
- Terminated control/ command/ and address bus

Options

- Operation temperature
 - Commercial 0°C ~ +85°C
- Package
 - 260-pin DIMM
- Frequency/CAS latency
 - 0.75ns @ CL = 19(DDR4-2666)

Table 1: Key Timing Parameters

Part Number	Industrial Nomenclature	Data Rate(MT/s)				
		CL19	CL18	CL17	CL16	CL15
PSD4B16HA26SN-16G	PC4-2666	2666	2400	2400	2133	2133
	PC4-2400		2400	2400	2133	2133
	PC4-2133				2133	2133

Table 2: Addressing

Parameter	16GB
Device Configuration	1024Mb x 8bit x 16 pcs
ROW address	32K (A[14:0])
Column address	1K (A[9:0])
Device bank group address	4 (BG[1:0])
Device bank address per group	4 (BA[1:0])

Table 3: DDR4-2666 Speed Bins

Speed Bin		DDR4-2666		Unit	Note
CL - nRCD - nRP		19-19-19			
Parameter	Symbol	min	max		
Internal read command to first data	t_{AA}	14.25	19	ns	
ACT to internal read or write delay time	t_{RCD}	14.25	-	ns	
PRE command period	t_{RP}	14.25	-	ns	
ACT to ACT or REF command period	t_{RC}	46.25	-	ns	
ACT to PRE command period	t_{RAS}	32	9 * t_{REFI}	ns	

Table 4: Operating Conditions
Recommended DC Operating Conditions – DDR4 (1.2V) operation

Symbol	Parameter	Rating			Units	Notes
		Min.	Typ.	Max.		
V_{DD}	Supply Voltage	1.14	1.2	1.26	V	1/2/3
V_{DDQ}	Supply Voltage for Output	1.14	1.2	1.26	V	1/2/3

Notes:

1. Under all conditions/ V_{DDQ} must be less than or equal to V_{DD} .
2. V_{DDQ} tracks with V_{DD} . AC parameters are measured with V_{DD} and V_{DDQ} tied together.
3. Under these supply voltages/ the device operates to this DDR4 specification.

Table 5: DRAM Component Operating Temperature Range

Symbol	Parameter	Rating	Units	Notes
T_{OPER}	Normal Operating Temperature Range	0 to 85	°C	1/2

Notes:

1. Operating Temperature T_{OPER} is the case surface temperature on the center / top side of the DRAM. For measurement conditions/ please refer to the JEDEC document JESD51-2.
2. The Normal Temperature Range specifies the temperatures where all DRAM specifications will be supported. During operation/ the DRAM case temperature must be maintained between 0-85°C under all operating conditions.

Table 6 : Absolute Maximum DC Ratings

Symbol	Parameter	Rating	Units	Notes
V _{DD}	Voltage on V _{DD} pin relative to V _{SS}	-0.4V~1.5V	V	1/3
V _{DDQ}	Voltage on V _{DDQ} pin relative to V _{SS}	-0.4V~1.5V	V	1/3
V _{IN} / V _{OUT}	Voltage on any pin relative to V _{SS}	-0.4V~1.4V	V	1
T _{STG}	Storage Temperature	-55 to +100	°C	1/2

Notes:

1. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
2. Storage Temperature is the case surface temperature on the center/top side of the DRAM. For the measurement conditions/ please refer to JESD51-2 standard.
3. V_{DD} and V_{DDQ} must be within 300mV of each other at all times; and V_{REF} must not be greater than 0.6*V_{DDQ}/ When V_{DD} and V_{DDQ} are less than 500mV; V_{REF} may be equal to or less than 300mV.

PCB Feature overview

➤ General

- * Board size: 69.6 x 30 mm ± 0.15 mm
- * Finished Board Thickness: 1.2 ± 0.1 mm

* Panel: 6 pieces PCB per panel

* 8-layer board

- * Impedance: 50/55 Ohm $\pm 10\%$ (Single-ended)
83/93 Ohm $\pm 15\%$ (Differential)

* Pin count: 260 PIN

➤ PCB Material

* Glass Epoxy FR4, .UL 94V-0, BP ML

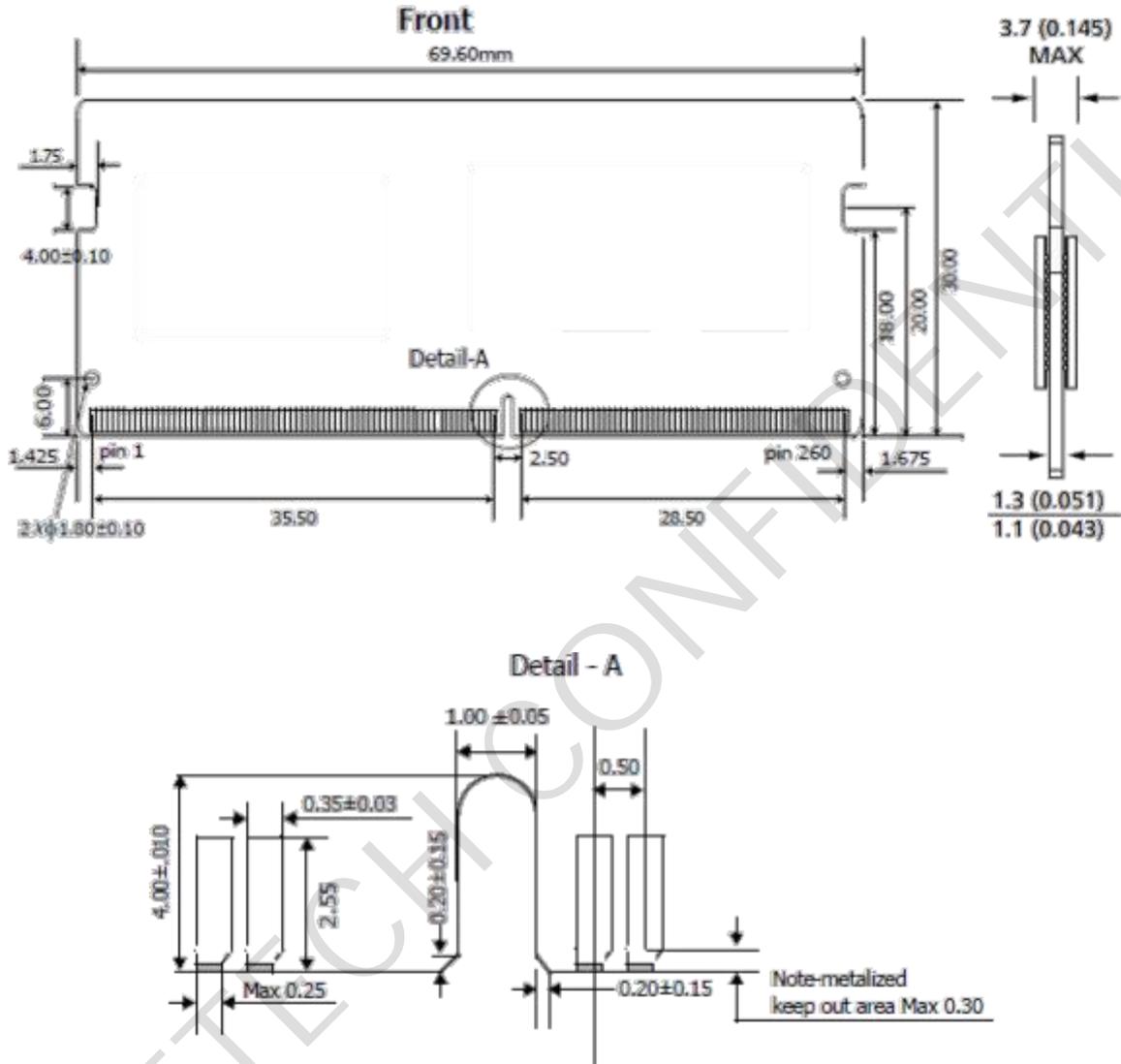
➤ Plating

* Edge Connector Plating: Nickel Followed by gold

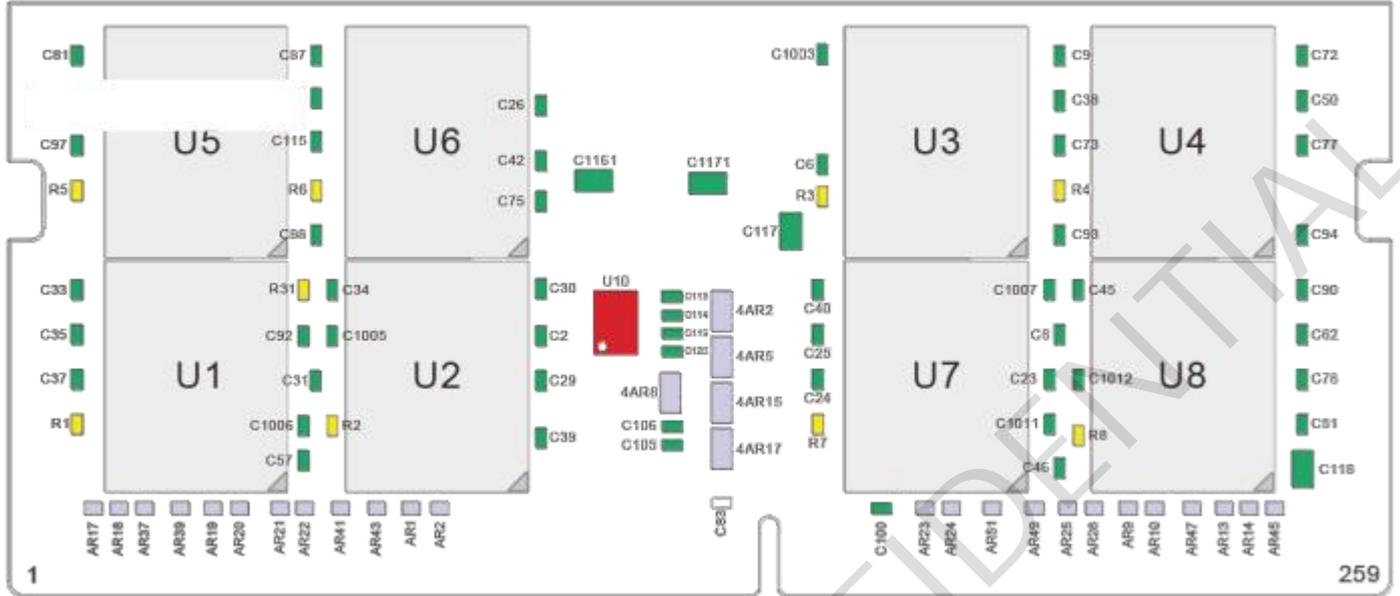
- Nickel Plating Thickness: 100 μ min.
- Surface treatment:
 - Gold Plating: 3 μ min.
 - SMT PAD: average 2~3 μ .

Module Dimensions

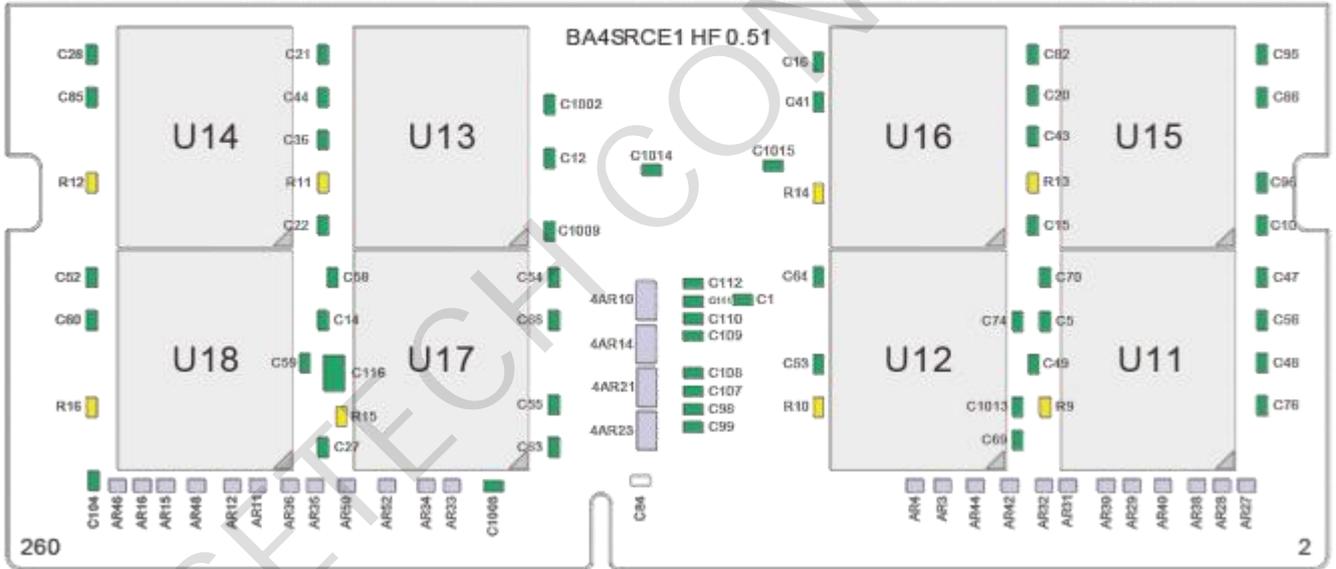
Figure 1: 260-pin DDR4 SO-DIMM



TOP SIDE 2-RANK WITHOUT ECC



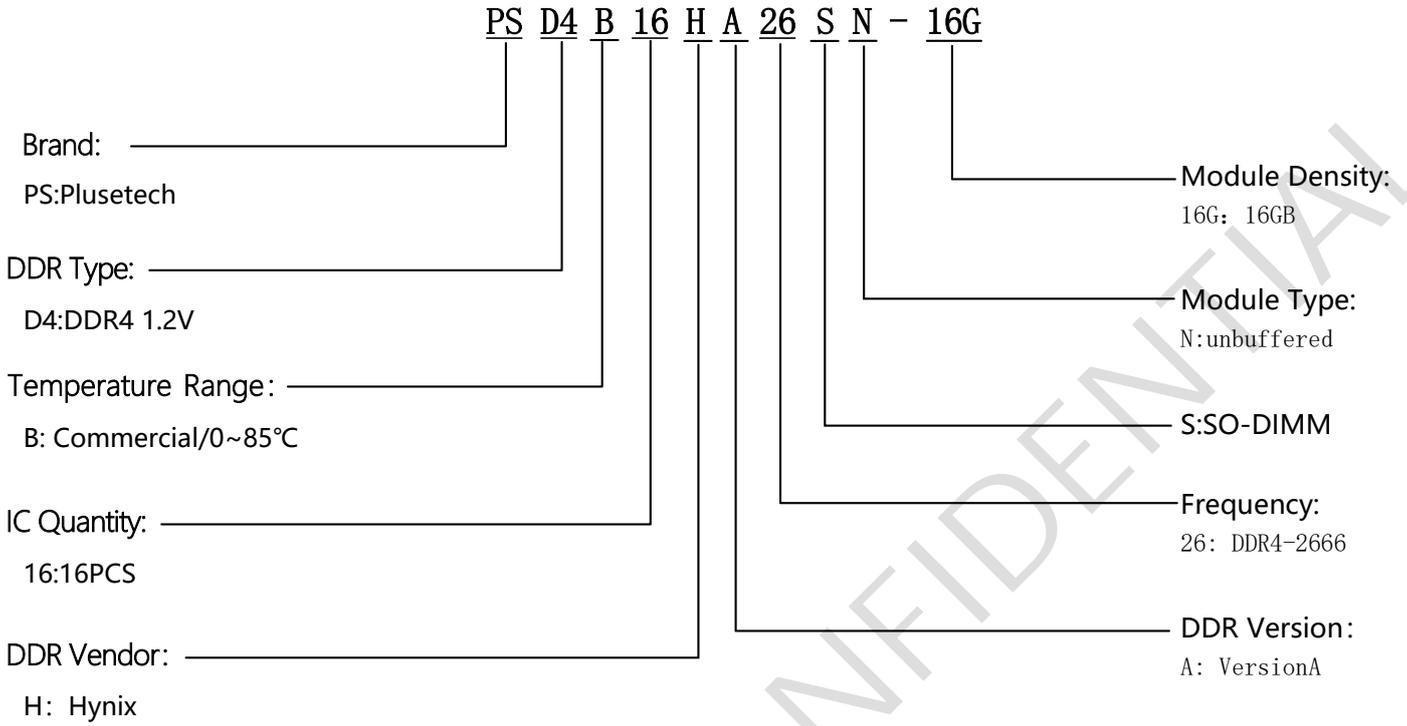
BOTTOM SIDE 2-RANK WITHOUT ECC



Notes:

1. All dimensions are in millimeters (mm)/ MAX/MIN or typical(TYP) where noted.
2. All dimensions diagrams is for reference only.

Part Number Decode



RevisionHistory

Rev.A0 - 12 / 22

- Initial release.